

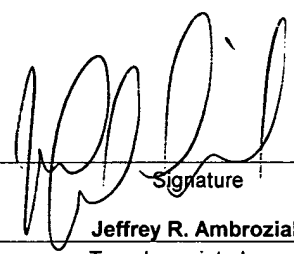
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PRE-APPEAL BRIEF REQUEST FOR REVIEW		Docket Number (Optional) 872.0100.U1(US)	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR		Application Number 10/005,766	
on <u>March 30, 2006</u>		Filed 11/02/2001	
Signature <u>Debra Pongetti</u>		First Named Inventor Antti Ruha	
Typed or printed name <u>Debra Pongetti</u>		Art Unit 2685	
		Examiner Tran, Pablo N.	
Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.			
This request is being filed with a notice of appeal.			
The review is requested for the reason(s) stated on the attached sheet(s). Note: No more than five (5) pages may be provided.			
I am the			
<input type="checkbox"/> applicant/inventor.		Signature	
<input type="checkbox"/> assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)		Jeffrey R. Ambroziak Typed or printed name	
<input checked="" type="checkbox"/> attorney or agent of record. Registration number <u>47,387</u>		(203) 925-9400 Telephone number	
<input type="checkbox"/> attorney or agent acting under 37 CFR 1.34. Registration number if acting under 37 CFR 1.34 _____		March 30, 2006 Date	
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.			

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This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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S.N.: 10/005,766
Art Unit: 2685



IN THE U.S. PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

APPLICANTS: Antti Ruha et al.

SERIAL NO.: 10/005,766

FILING DATE: 11/02/2001

EXAMINER: Pablo N. Tran

ART UNIT: 2685

ATTORNEY'S DOCKET NO.: 872.0100.U1(US)

TITLE: MULTI-MODE I/O CIRCUITRY SUPPORTING LOW INTERFERENCE
SIGNALING SCHEMES FOR HIGH SPEED DIGITAL INTERFACES

PRE-APPEAL BRIEF REQUEST FOR REVIEW ATTACHMENT

The following is a concise recitation of a clear error in the Examiner's rejections in this application.

1. In the final Office Action of November 30, 2005, the Examiner rejected claims 1-2, 5-6, 8-9, 12-14, 17, 20, 23-24, 39-40, 43-44, and 46-47 as being anticipated by Hedberg (5,994,921). The Examiner asserted that Hedberg discloses that "the IC are constructed with CMOS-based transistors that are selectively interconnected together by switches to operate as two single-ended, voltage mode links, and as a single differential voltage mode link (fig. 5, fig. 6, col.1/ln. 15-29, col. 5/ln. 42-col. 6/ln. 8)."

In the Advisory Action of March 14, 2006 the Examiner reasserted that "Furthermore, Hedberg disclose such transmit/receive I/O circuits (fig. 5-6) that selectively interconnectable to operate in either single-ended, voltage mode or differential voltage mode (col. 1/ln. 15-48, col. 2/ln. 7-28, especially col. 2/ln. 22-28)." Applicants respectfully argue that the Examiner's citations to Hedberg do not support this element of claim 1.

Applicants resubmit, in their entirety, the arguments put forth in the Response of February 17, 2006 at page at page 11, line 5 to page 12, line 30 pertaining to this issue. Applicants further wish to draw attention to the last two sentences of the detailed Description, immediately prior to the recitation of the claims, wherein Hedberg discloses "Major principles of the invention apply also to single-ended sender devices. Single-ended signaling is well known in the art." This statement is consistent with the remainder of the specification

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wherein Hedberg fails to disclose a single embodiment of the invention directed to single-ended signaling. As no such signaling mode is previously disclosed, it logically follows, as is the case, that Hedberg does not disclose a circuit "selectively interconnected together by switches to operate as two single-ended, current or voltage links, and as a single differential current or voltage mode link" as recited..